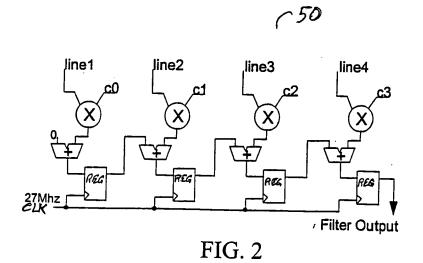


Host Interface

FIG. 1



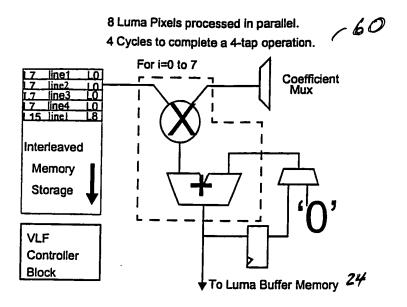


FIG. 3

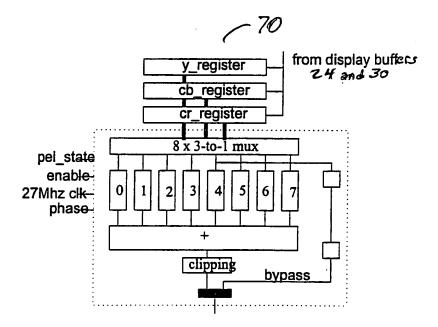


FIG. 4

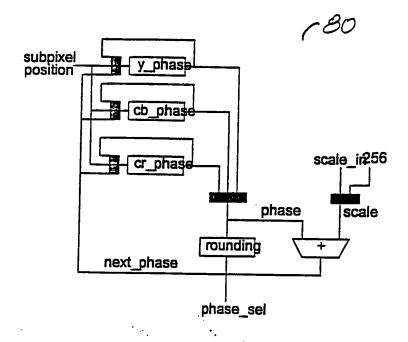


FIG. 5

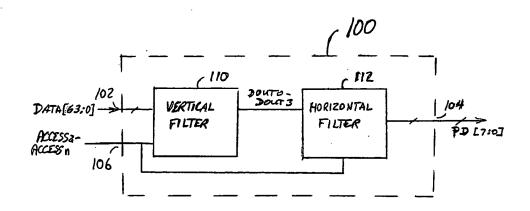


FIG. 6

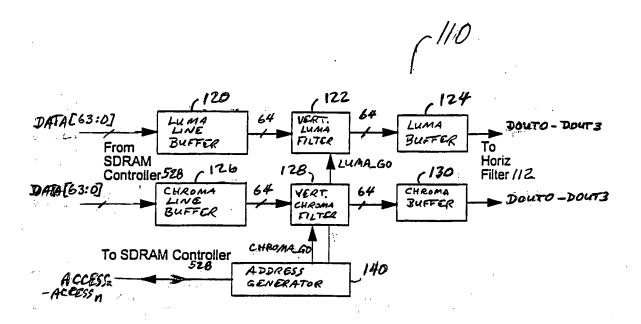


FIG. 7

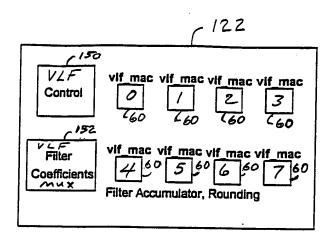


FIG. 8

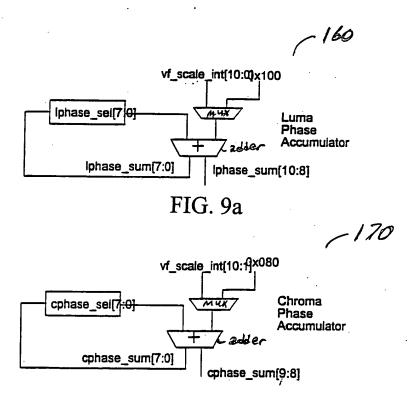


FIG. 9b

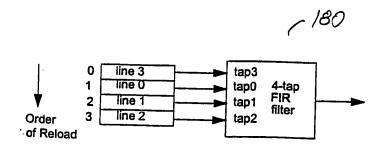


FIG. 10

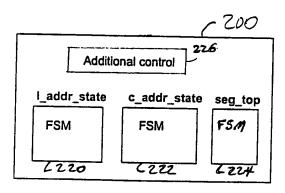


FIG. 11

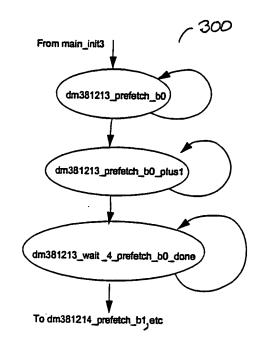
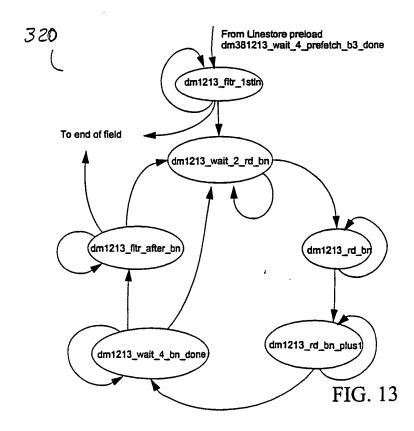


FIG. 12



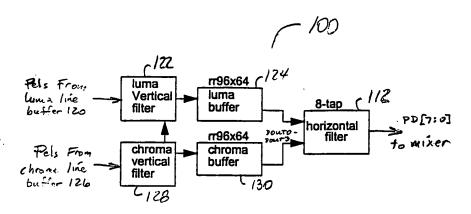


FIG. 14

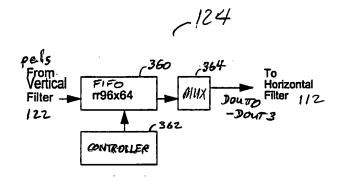


FIG. 15

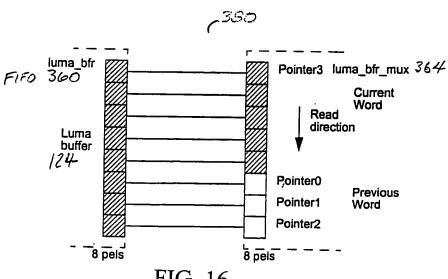
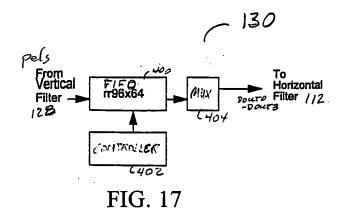
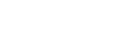


FIG. 16





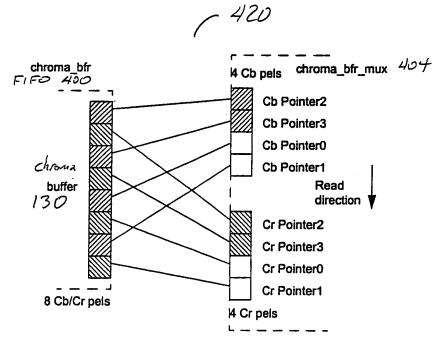


FIG. 18

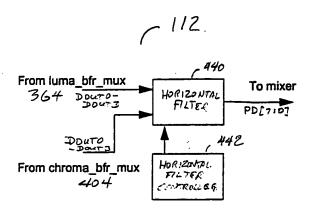


FIG. 19

- 450

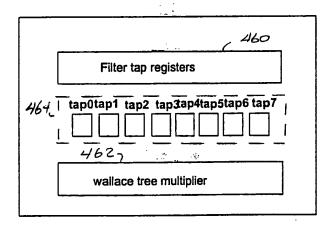


FIG. 20

j

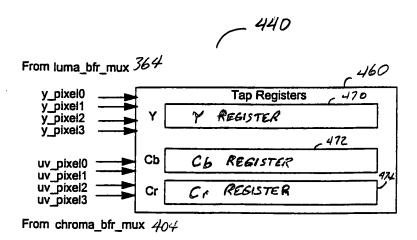


FIG. 21

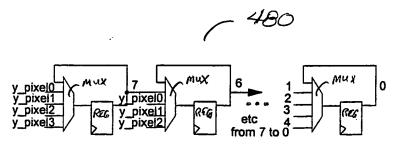


FIG. 22

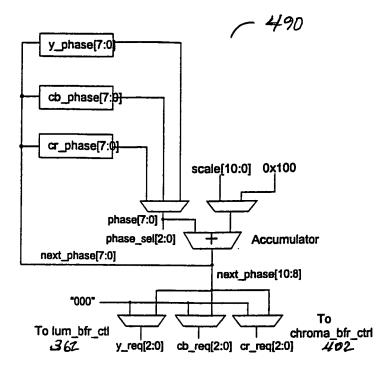


FIG. 23

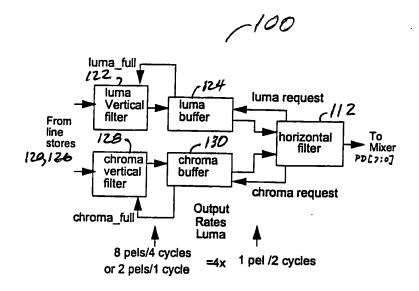


FIG. 24

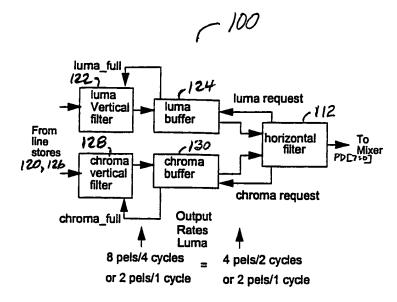


FIG. 25

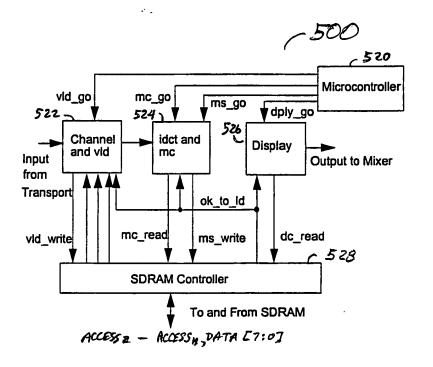


FIG. 26

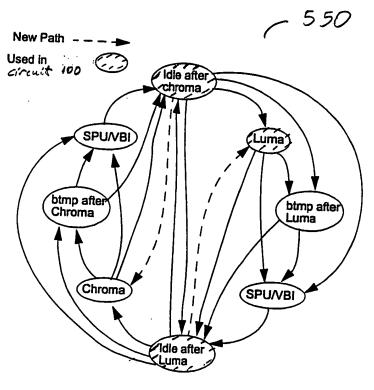


FIG. 27

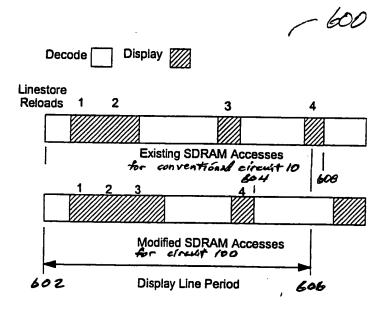


FIG. 28

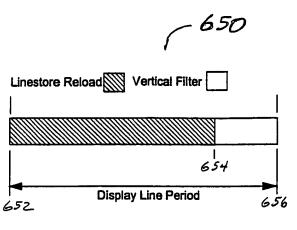


FIG. 29

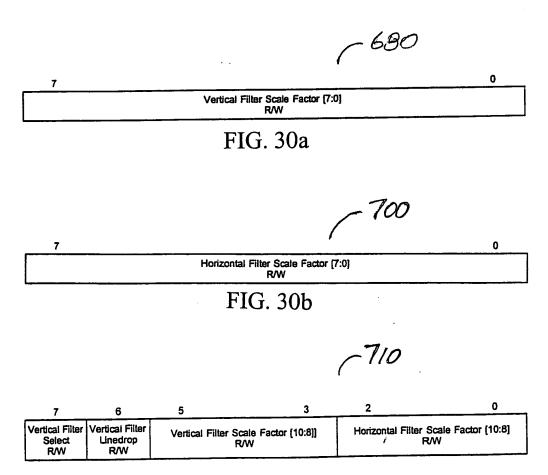


FIG. 31

| | 720 | |
|----|---|---|
| 7 | | 0 |
| ·. | Vertical Filter Top Field Phase Origin [7:0] RW | |
| | FIG. 32a | |
| | 730 | |
| 7 | | 0 |
| | Vertical Filter Bottom Field Scale Factor Origin [7:0] R/W | |
| | | |

FIG. 32b